

Typical Applications

*Telecommunication
Universal Clock*

Features

*5x7 ceramic oscillator
Enable Function*

Frequency range

1 MHz – 70 MHz

Frequency stabilities

Parameter	Code I	Frequency stability	Operating temp range
Overall (vs. Initial, vs. Temperature, vs. Load, vs. Supply, vs. 1 year aging)	54	± 100 ppm	0...70 °C
	55	± 50 ppm	
	56	± 25 ppm	

RF output

Parameter	Value	Condition
Signal	CMOS	Note 1
Load	15pF	
Duty cycle	40/60%	@ Vs/2

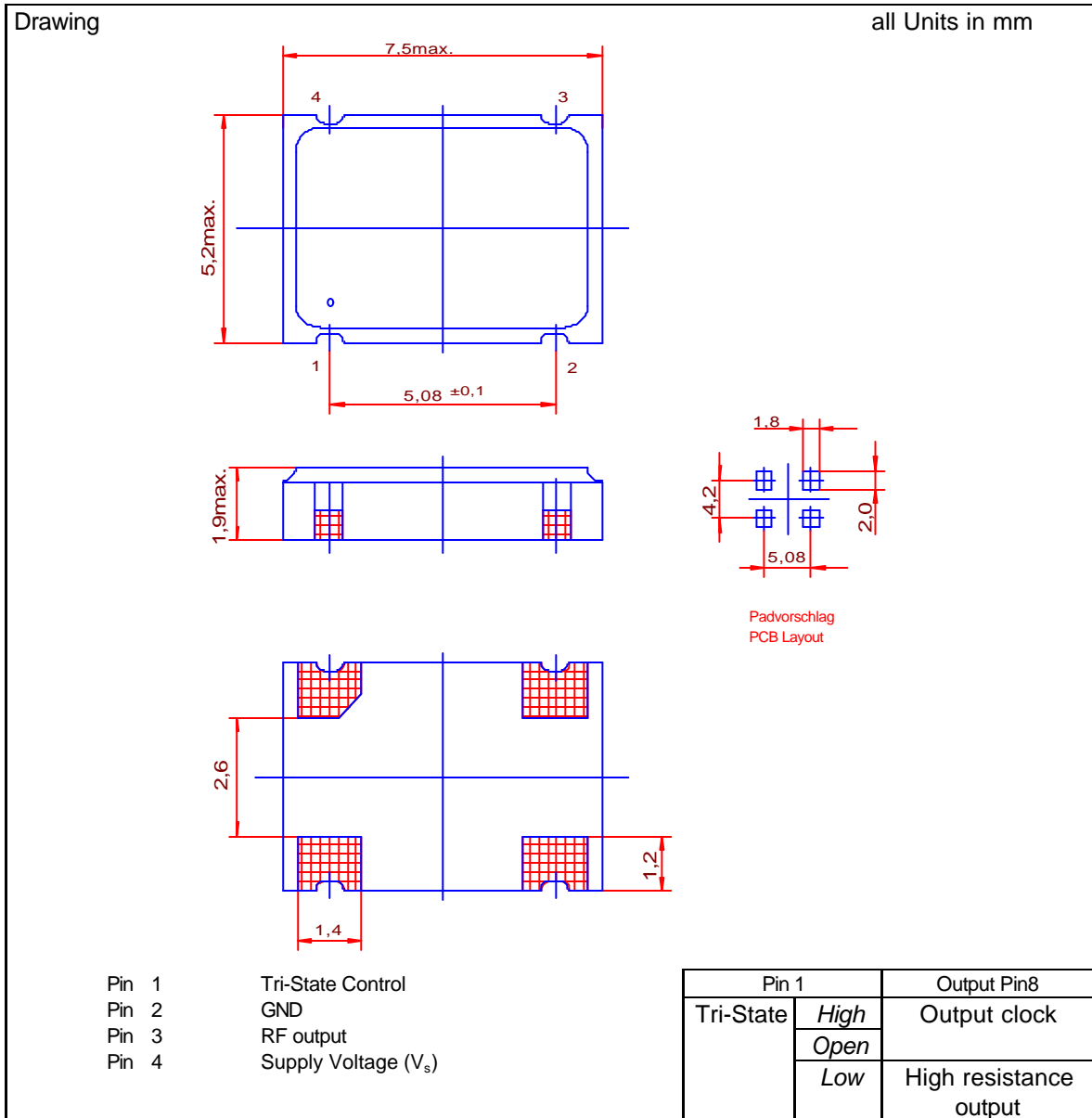
Supply Voltage

Parameter	Code II	Value	Condition
Supply voltage (Vs) Power consumption steady state	05	5V ± 5%	< 20 MHz < 70 MHz
		< 35 mA < 45 mA	
Supply voltage (Vs) Power consumption steady state	33	3.3V ± 5%	< 20 MHz < 70 MHz
		< 25 mA < 40 mA	

Additional parameters

Parameter	Value	Condition
Frame Specification	TQA 9000	

Enclosure



Ordering Code	Code I	Codell	
Model	Frequency Stability	Supply	Frequency
Example: AD	54.	05.	30M00000
Order: AD			

Notes

- can drive TTL, AC MOS and CMOS
Unless otherwise stated all values are valid after warm-up time and refer to typical conditions for supply voltage, frequency control voltage, load, temperature (25°C)
Subject to technical modification; Not all options are available at all Frequencies